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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Tatsuji NAGAOKA

Serial No.: 10/781,360

Filed: February 18, 2004

Title: MIS-TYPE SEMICONDUCTOR  
DEVICE

Group Art Unit: 2811

Examiner:

Attorney Docket No.: FUJI:283

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Marc A. Rossi

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Sir:

A corrected filing receipt is hereby requested in view of an error which appears in the original. Namely; the Application Number of one of the Priority documents listed under Foreign Applications was wrong. For the convenience of the Patent and Trademark Office, attached is a photocopy of the original receipt on which the error has been noted in red. Also enclosed are copies of both of the applications cover sheet.

Respectfully submitted,  
ROSSI, KIMMS & McDOWELL LLP

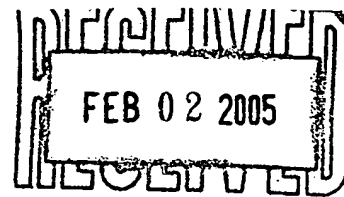
Date : 04/22/05

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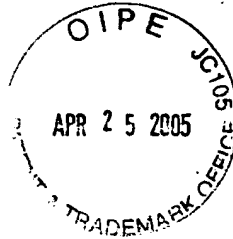
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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/781,360	02/18/2004	2811	1120	FUJI:283	16	24	3

37013  
ROSSI & ASSOCIATES  
P.O. BOX 826  
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CONFIRMATION NO. 9256  
UPDATED FILING RECEIPT



\*OC000000015078966\*

Date Mailed: 02/01/2005

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**Applicant(s)**

Tatsuji Nagaoka, Nagano, JAPAN;

**Power of Attorney:**

Marc Rossi-31923

**Domestic Priority data as claimed by applicant**

**Foreign Applications**

063161  
JAPAN JP 2003-063061 03/10/2003  
JAPAN JP 2003-197644 07/16/2003

If Required, Foreign Filing License Granted: 10/14/2004

The country code and number of your priority application, to be used for filing abroad under the Paris Convention, is **US10/781,360**

Projected Publication Date: 05/12/2005

Non-Publication Request: No

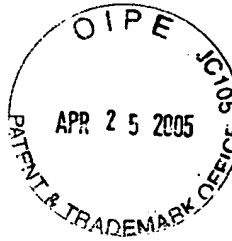
Early Publication Request: No

Title

MIS-type semiconductor device

Preliminary Class

257



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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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This is to certify that the annexed is a true copy of the following application as filed  
with this Office.

出願年月日  
Date of Application:

2003年 3月10日

出願番号  
Application Number:

特願2003-063161

[ST. 10/C]:

[JP 2003-063161]

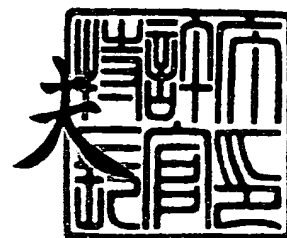
願人  
Applicant(s):

富士電機デバイステクノロジー株式会社

2004年 2月 6日

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出 願 年 月 日                      2 0 0 3 年    7 月 1 6 日  
Date of Application:

出 願 番 号                      特 願 2 0 0 3 - 1 9 7 6 4 4  
Application Number:  
[ J P 2 0 0 3 - 1 9 7 6 4 4 ]

願                      人                      富士電機デバイステクノロジー株式会社  
Applicant(s):

2 0 0 4 年    2 月    6 日

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